

F I G. 3

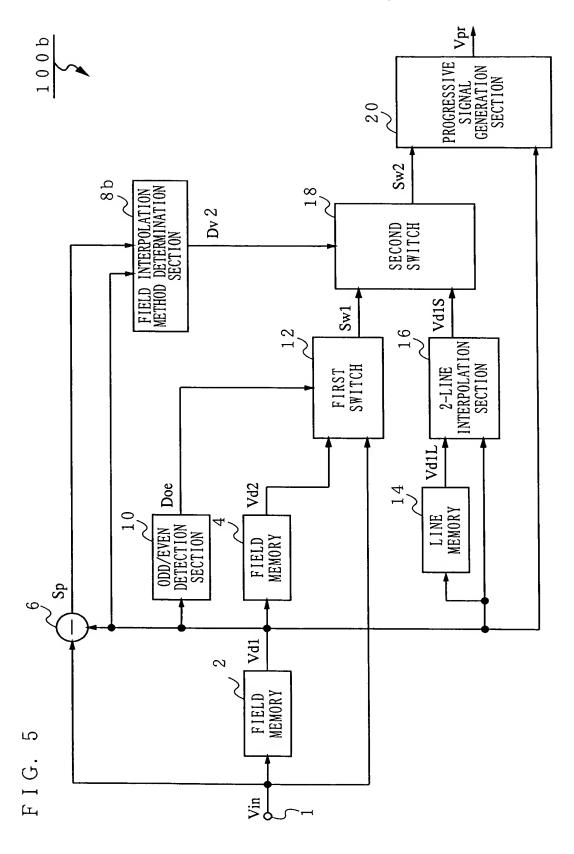
| REGISTER 8 5 | REGISTER 86 | REGISTER 87 | REGISTER 88 | | |
|-----------------|----------------|----------------|----------------|---------------|-------------------------------|
| R1 | R2 | R3 | R4 | COUNTER INPUT | |
| 0 | 1 | 0 | 1 | INCREMENT | Тс |
| 1 | 0 | 1 | 0 | INCREMENT | |
| 0 | 0 | 0 | 0 | CANCEL | |
| О | 0 | 0 | 1 | CANCEL | |
| 0 | O | 1 | 0 | CANCEL | T 1 |
| 0 | 1 | 0 | 0 | CANCEL | |
| 1 | 0 | O | 0 | CANCEL | |
| 0 | 0 | 1 | 1 | RESET | |
| 0 | 1 | 1 | 0 | RESET | |
| 0 . | 1 | 1 | 1 | RESET | |
| 1 | O | 0 | 1 | RESET | $\int_{-\infty}^{\infty} T r$ |
| 1 | 0 | 1 | 1 | RESET | |
| 1 | 1 | O | 0 | RESET | |
| 1 | 1 | 0 | 1 | RESET | |
| 1 | 1 | 1 | 0 | RESET | |
| 1 | 1 | 1 | 1 | RESET | |

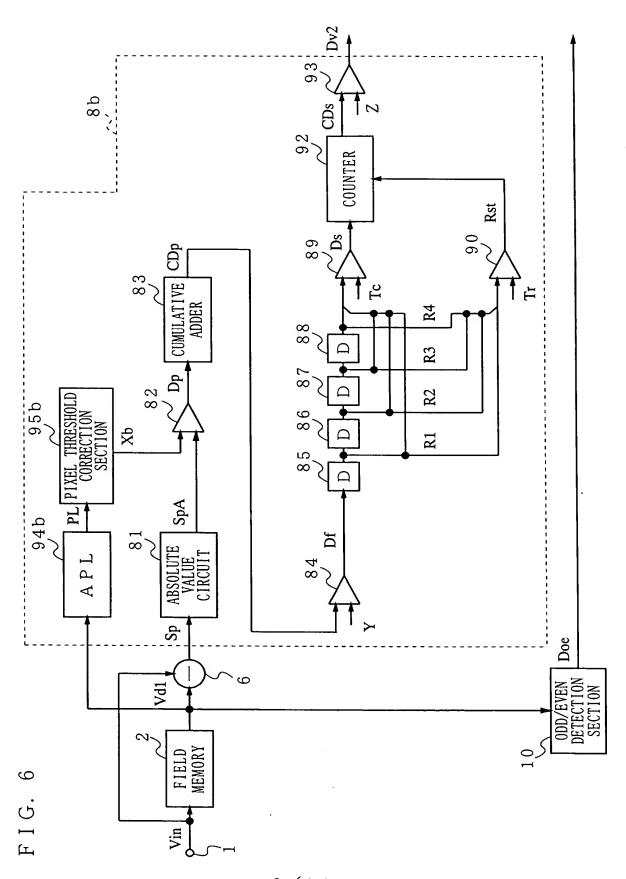
: : : : : : : 2 \sim C 1 1(E)9 О \circ \circ \circ : 000 2 2 2 വ 0 Ö \circ \circ Μ \circ က 1(E)C1B 1 2 \sim 4 В $\mathbf{\alpha}$ \mathfrak{A} 000 \sim 2 2 က 0 α В Μ ď m B 2 2 2 A 1 B 1 1(E)2 A Ø Ø A 2 000 0 A K A 1 PARENT FRAME FRAME No. INPUT INTERLACED SIGNAL Vin 2-FIELD DELAY INPUT INTERLACED SIGNAL Vd2 INTERPOLATION SIGNAL Sw2 INTER-FIELD CORRELATION DETERMINATION SIGNAL Df FOR Vin AND Vd1 1-FIELD DELAY INPUT INTERLACED SIGNAL VdI (INTERPOLATION TARGET SIGNAL) FIELD No. FIELD IDENTIFICATION SIGNAL DOE FOR 1-FIELD DELAY INPUT INTERLACED SIGNAL Vd1 FIELD No. I-FIELD DELAY INPUT INTERLACED SIGNAL Vd1

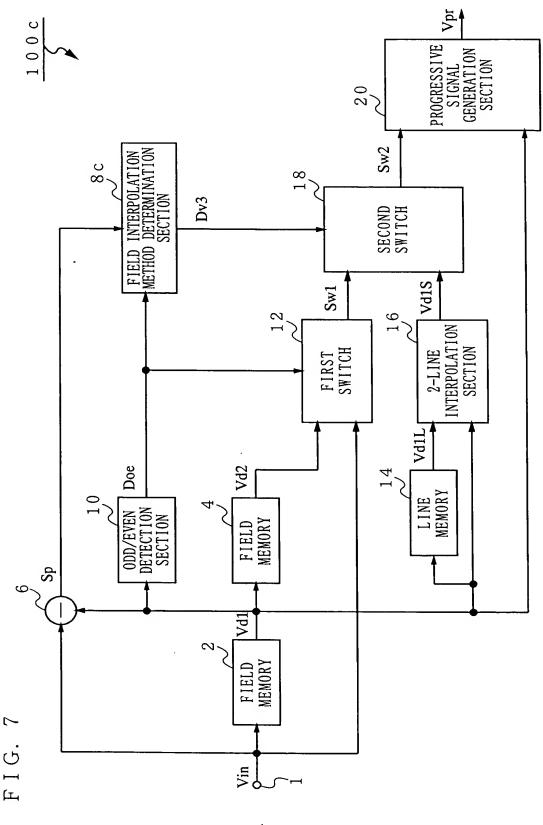
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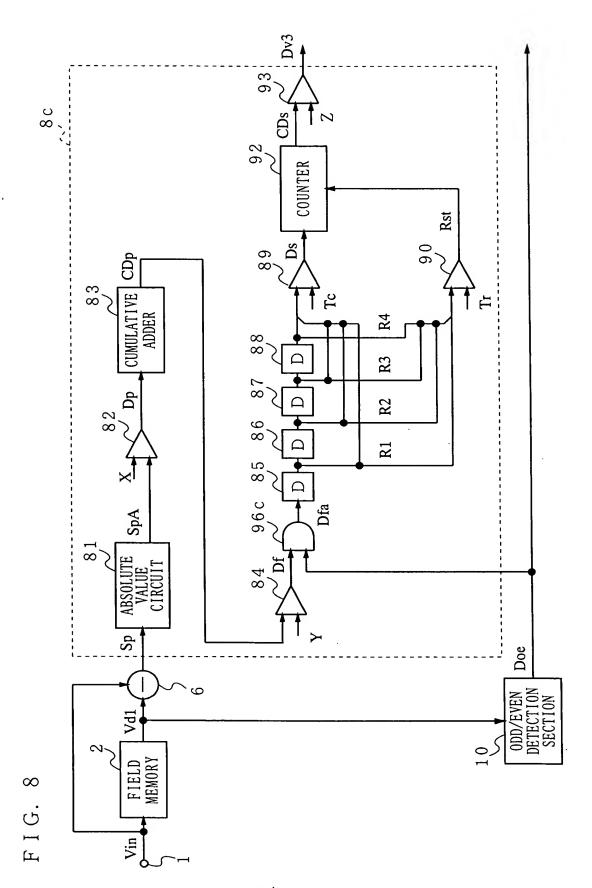
FIG.

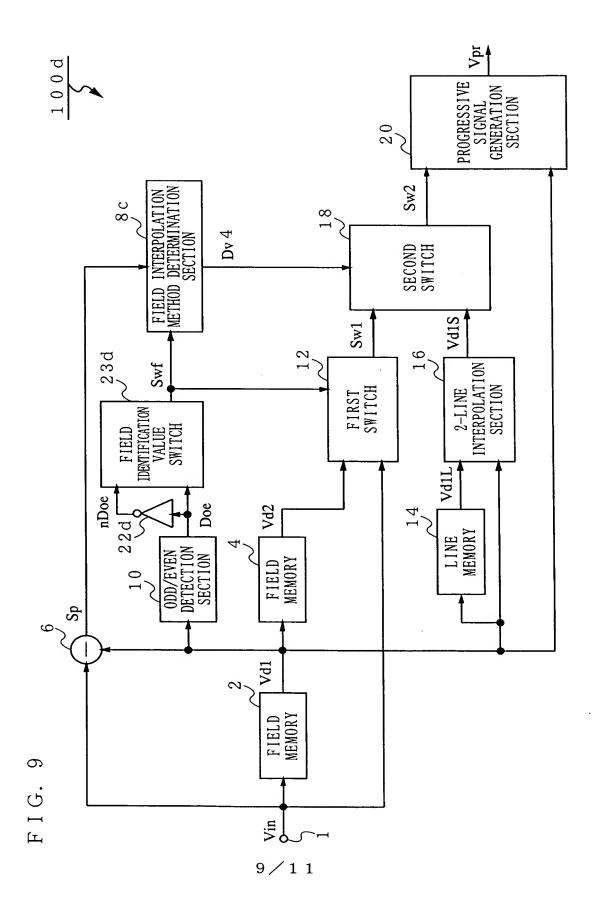


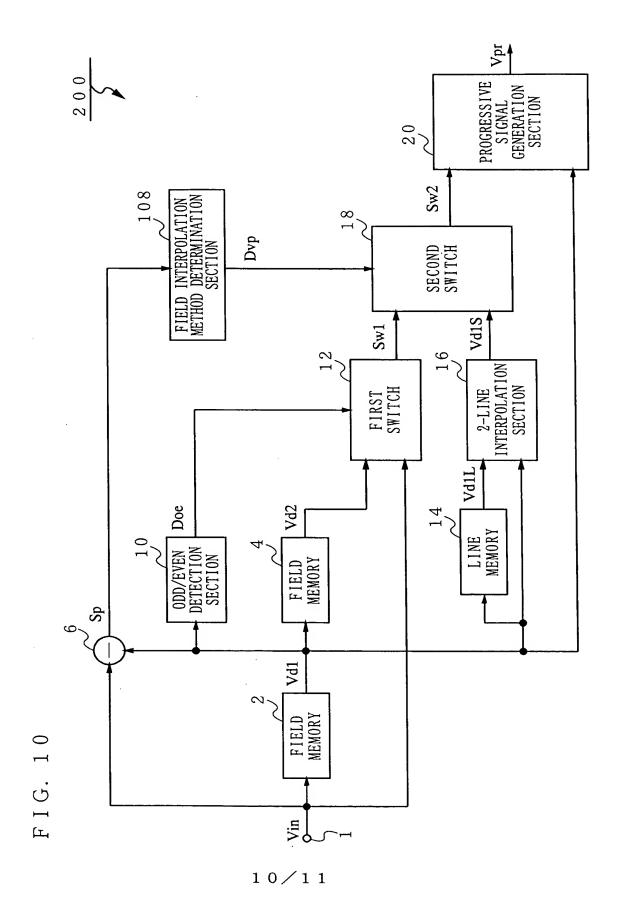




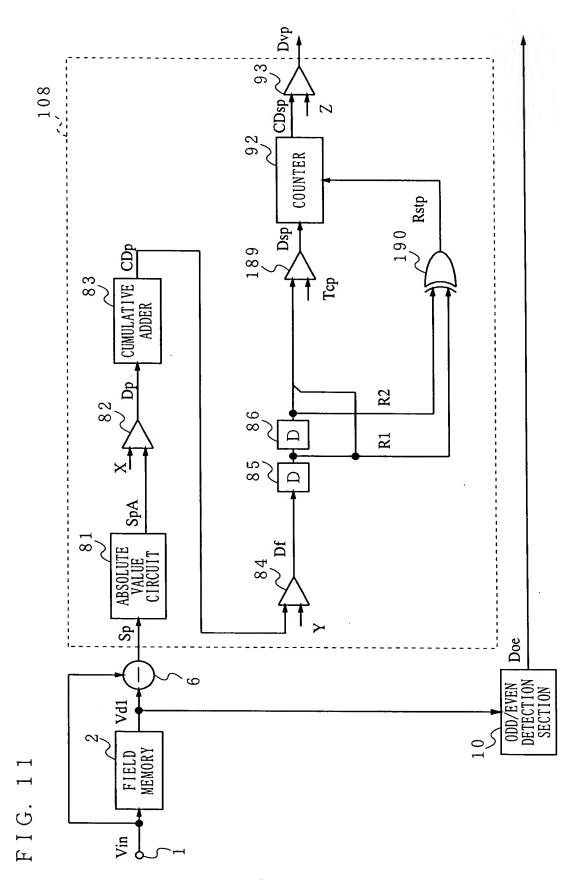
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